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Filing Date	December 22, 2005
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Art Unit	Not Yet Assigned
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Attorney Docket Number	2003P08945WOUS
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Sheet 1 of 2

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9/8/08

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Substitute for form 1449B/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 2 of 2

Complete if Known

Application Number	Not Yet Assigned
Filing Date	December 22, 2005
First Named Inventor	Axel Andersch
Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned
Attorney Docket Number	2003P08945WOUS

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
KTV	3	LISA GUERRA, JOACHIM FITZNER, DIPANKAR TALUKDAR, CHRIS SCHLÄGER, BASSAM TABBARA and VOJIN ZIVOJNOVIC, "Cycle and Phase Accurate DSP Modeling and Integration for HW/SW Co-Verification, Design Automation Conference, 1999 Proceedings, 36 th New Orleans, LA, USA, June 21-25, 1999, Piscataway, NJ, IEEE, June 21, 1999, Pages 964-969, XP010344032.	
KTV	4	P. LE MARREC, C.A. VALDERRAMA, F. HESSEL and A.A. JERRAYA, "Hardware, Software and Mechanical Cosimulation for Automotive Applications", Rapid System Prototyping, 1998, Proceedings, 1998 Ninth International Workshop on Leuven, Belgium, June 3-5, 1998, Los Alamito, CA, USA, IEEE Compt. Soc, June 3, 1998, Pages 202-206, XP010283205.	

Examiner Signature	/Kandasamy Thangavelu/	Date Considered	9/8/08
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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